

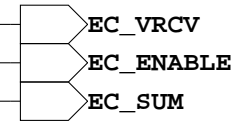
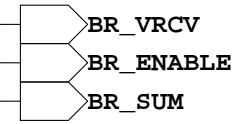
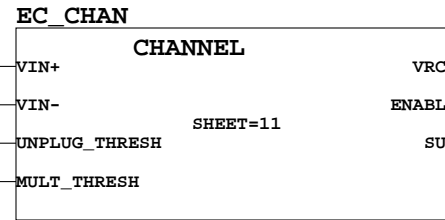
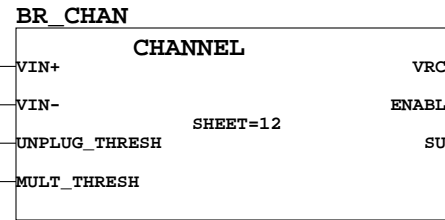
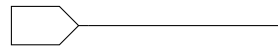
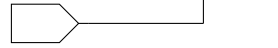
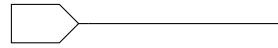
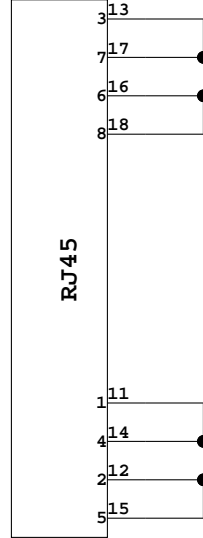
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SHEET=101

<b>Indiana University</b> <b>Physics Department</b>	
ATLAS TRT Hardware Interlock Logic Box	
Receive Board ELMBs	
Paul Smith	June, 2007
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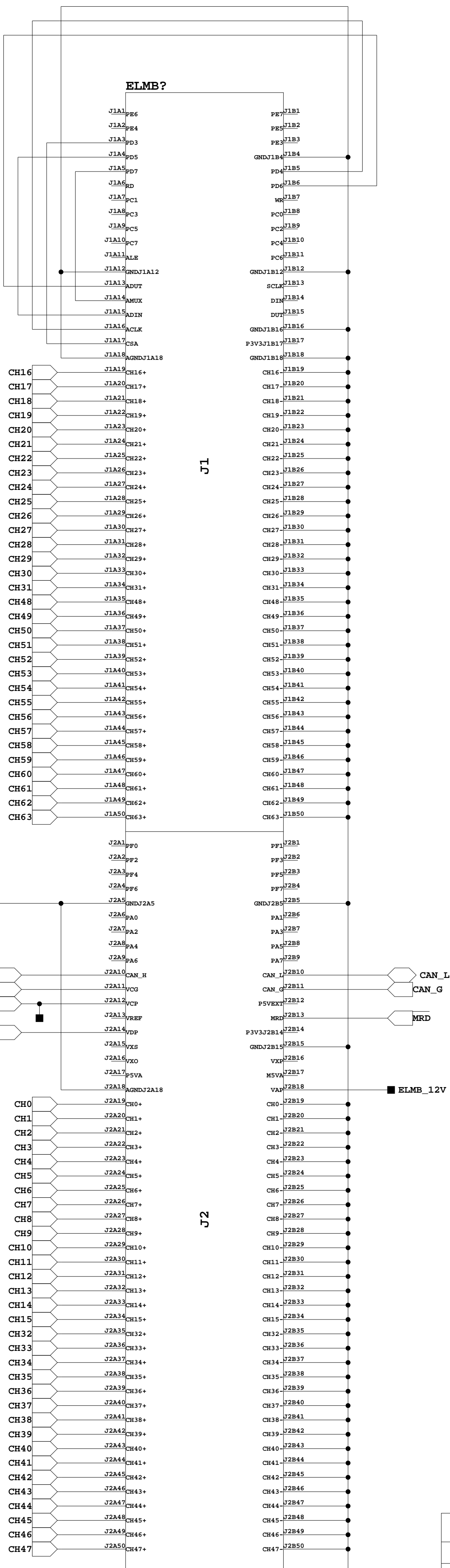
J?

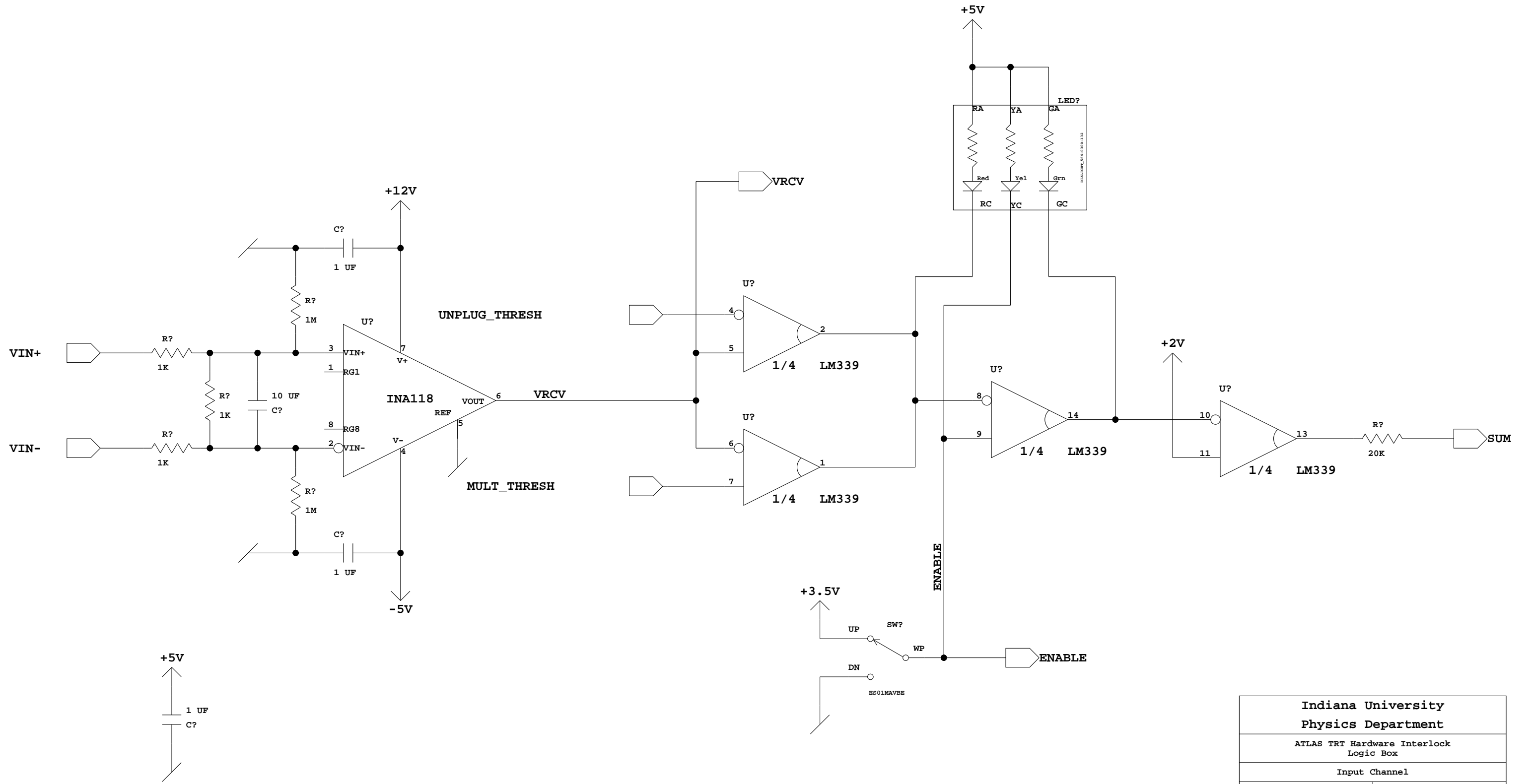


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ATLAS TRT Hardware Interlock Logic Box	
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